Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("5319591").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 09:41
L2	2	("4045781").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 09:45
L3	4661	(711/5,100,104,105,170).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/20 11:16
L4	24639	memory adj module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:16
L5	615184	module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:16
L6	736	3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:17
L7	1598	3 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:16
L8	287107	impedance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:17
L9	1869218	voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:17

L10	62	6 and 8 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 11:17

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  - L1: (2) ("5319591").PN.
  - L2: (2) ("4045781").PN.
  - L3: (4661) (711/5,100,104,105,170).CCLS.
  - L4: (24639) memory adj module
  - L5: (615184) module
  - L7: (1598) 3 and 5
  - L6: (736) 3 and 4
  - L8: (287107) impedance
  - L9: (1869218) voltage
  - L10: (62) 6 and 8 and 9
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- Queue
- Trash

	U	1	Document ID	Issue Date	Pages	Inventor	Title	Current OR	Cu
1			US 5802395 A	19980901	9	Connolly; Brian J. et al.	High density memory modules with improved data bus performa	710/14	365 710

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